

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of: )  
Shunpei YAMAZAKI et al. )  
Serial No. 08/520,079 )  
Filed: 08/28/1995 )  
For: SEMICONDUCTOR CIRCUIT FOR ELECTRO- )  
OPTICAL DEVICE AND METHOD OF MANUFACTURING )  
THE SAME )

Examiner: 2815  
Group Art Unit: P. Brock II

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on 8/9/02

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, DC 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c)

It is requested that the accompanying information disclosure statement be considered and made of record in the above-captioned application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By: \_\_\_\_\_

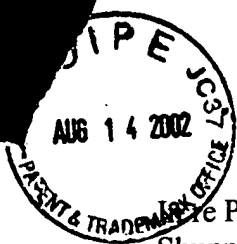
Luan C. Do  
Registration No. 38,434

NIXON PEABODY LLP  
8180 Greensboro Drive, Suite 800  
McLean, Virginia 22102  
Telephone: (703) 770-9300

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